# Industrial Process Controllers and Simulators

#### Topic 3

Organization and structure of computers for control purposes

# **Controller-Object Interaction**

#### 1. Signal exchange

- ▶ type of signals
- ▶ hardware I/O systems
- ▶ time-driven & event driven exchange

#### 2. Interaction with the object

## **Controller-Object Interaction**

- 1. Signal exchange
  - ▶ type of signals
  - ▶ hardware I/O systems
  - ▶ time-driven & event driven exchange
- 2. Interaction with the object

## Signal Exchange

# Types of signals: ♦ Analogous Contir ♦ Discrete ♦ Pulse The i Discrete Time, dt = 0.5 sec.



# Signal Exchange

#### Two forms:

- ♦ Time-Driven
- ♦ Event-Driven

The interaction takes place at nredefined maments of time.
The interaction takes place when a certain event occurs



## **Hardware I/O Systems**

- Analogous Input Subsystems (AIS)
- Analogous Output Subsystems (AOS)
- Discrete Input Subsystems (DIS)
- Discrete Output Subsystems (DOS)

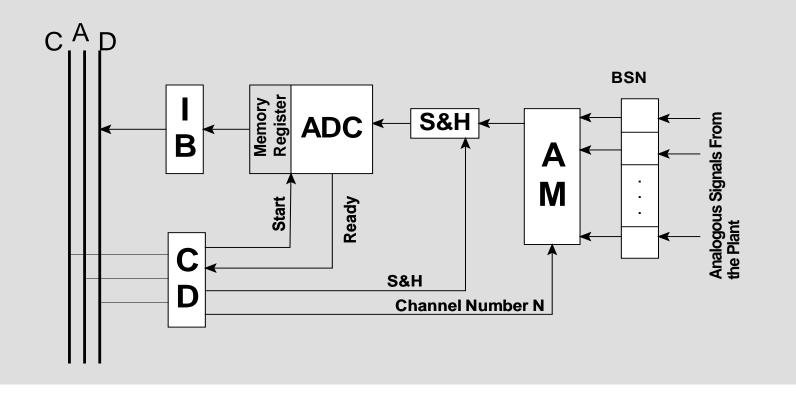
#### **AIS** elements:

- ♦ ADC Analogous-to-Discrete Converter
- ♦ BSN Block for Signal Normalization
- ♦ S&H Sample and Hold
- ♦ AM Analog Multiplexer
- ♦ CU Control Unit
- ♦ IB Interface Buffer
- ♦ C, A, D Control, Address and Data Bus

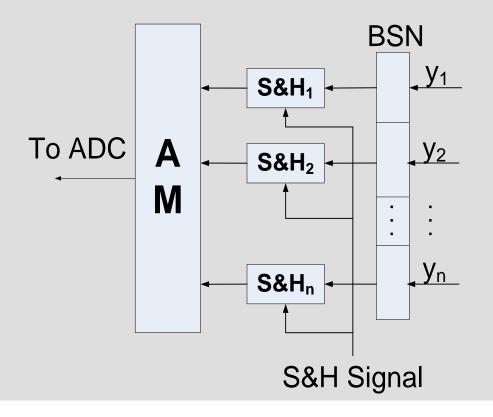
#### **Realizations of AIS:**

- with a single ADC element
- with multiple ADC elements
- with multiple S&H elements
- with Digital Sensors (DS), instead of ADC

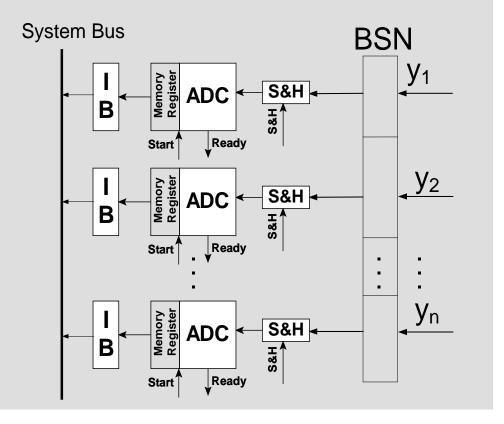
#### **AIS** with single ADC Element



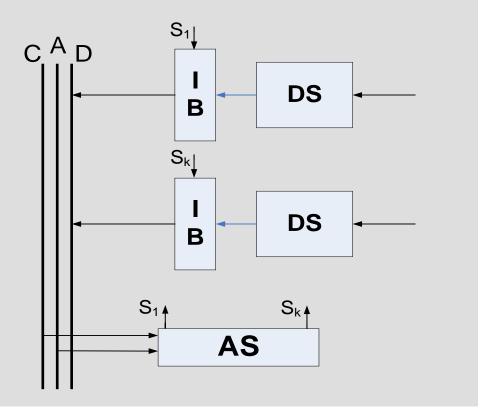
#### AIS with multiple S&H elements



#### **AIS** with multiple ADC elements



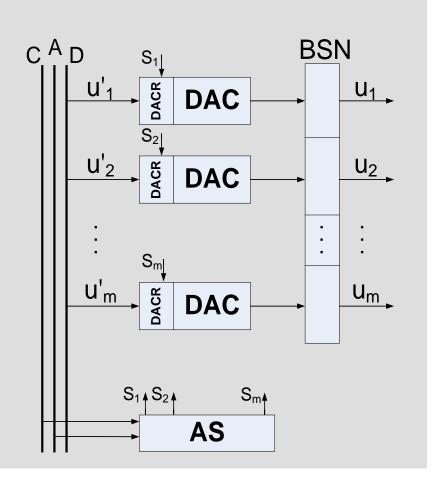
#### **AIS with Digital Sensor**



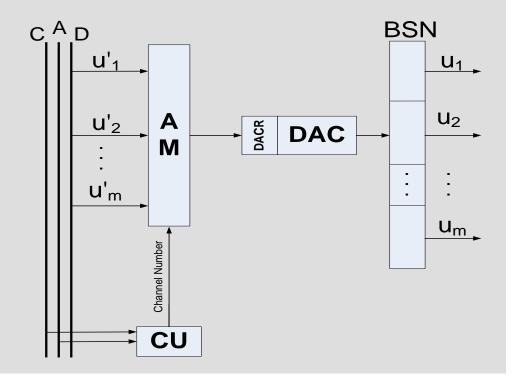
#### **AOS elements:**

- 1. Output devices -> generally two kinds are used:
  - ◆ DAC Digital-to-Analogous Converter
  - ♦ PWM Pulse-Width Modulation
- 2. SHB Sample and Hold Block
- 3. AS Address Selector
- 4. DACR DAC Register

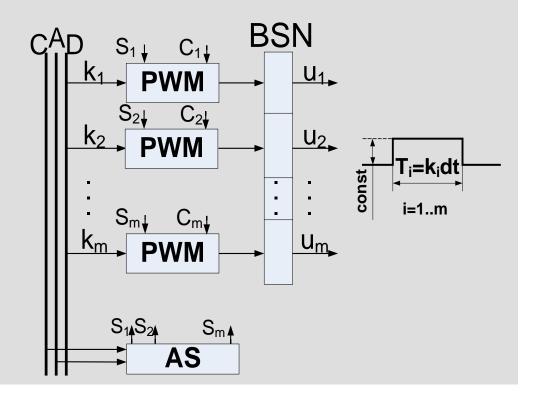
#### **Basic organization**



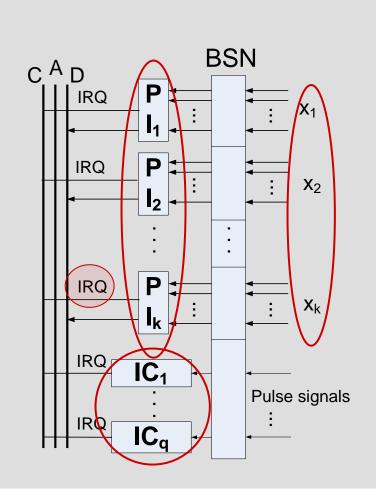
#### **AOS** with single DAC



#### **AOS** with Pulse-Width Modulators



#### Structure of DIS



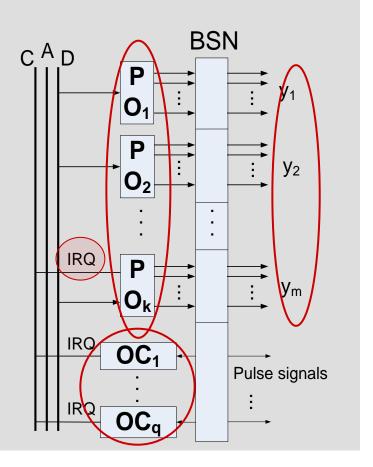
#### **DIS** elements:

- ♦ PI Parallel Input
- IRQ Interrupt Request
   Line
- ♦  $X = Ux_i$  (i=1..k) The set of discrete input signals
- ♦ IC Input Pulse Counter
- BSN Binary Signals
   Normalization

#### Structure of DOS

#### **DOS elements:**

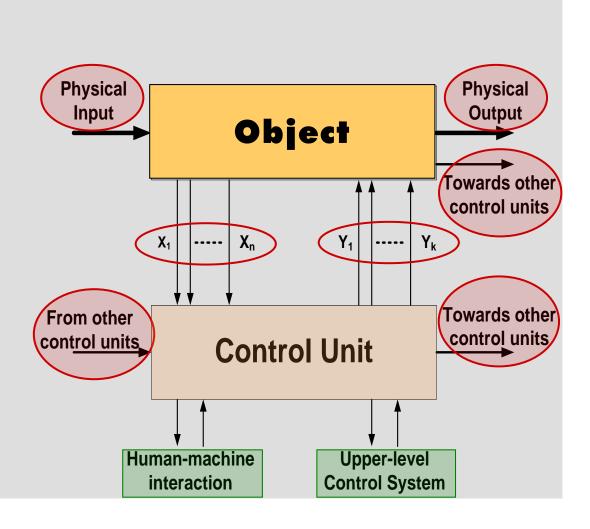
- ♦ PO Parallel Output
- ♦ IRQ Interrupt Request Line
- ♦ Y = Uy<sub>i</sub> (i=1..m) The set of discrete output signals
- ♦ OC Output Counter





# Structure of a Cyber-Physical System

# Physical organization



### **Control Systems**

#### Main types:

Continuous

Discrete

♦ Hybrid

Continuous interaction signals – they

Discrete signals in both directions

The plant operates in a mixed mode





# **Control Systems**

#### Main types:

- ♦ Continuous
- Discrete
- ♦ Hybrid

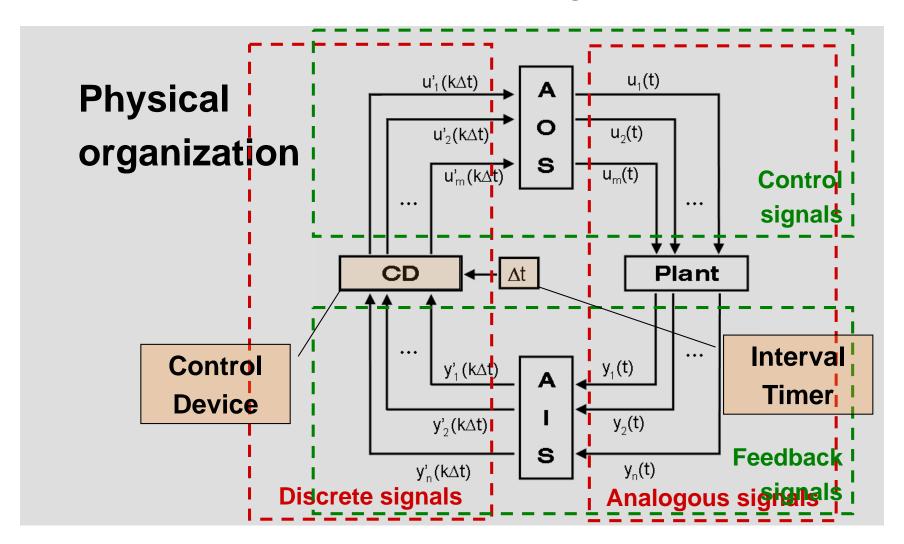


#### Main types:

- ♦ Continuous
- ♦ Discrete
- ♦ Hybrid

#### Interaction:

- ♦ The controller is a discontinuous machine.
- ♦ Both incoming and outgoing signals from the controller are continuous.
- ♦ Requires analogous I/O subsystems
- ♦ Two types of signal quantifying
  - ▶ By level
  - ► By time



#### **Signal Conversions:**

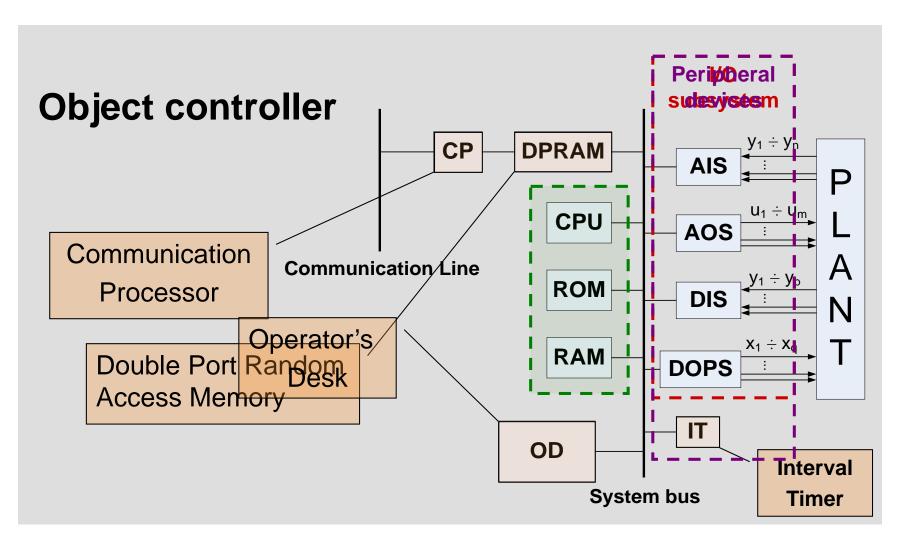
- Quantifying by level
- Quantifying by time
- ♦ Discrete-to-analogous

Performed by the ADC

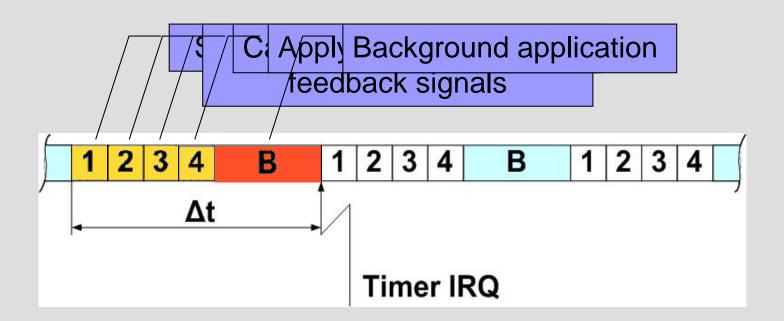
**Performed by timers** 

Performed by the DAC





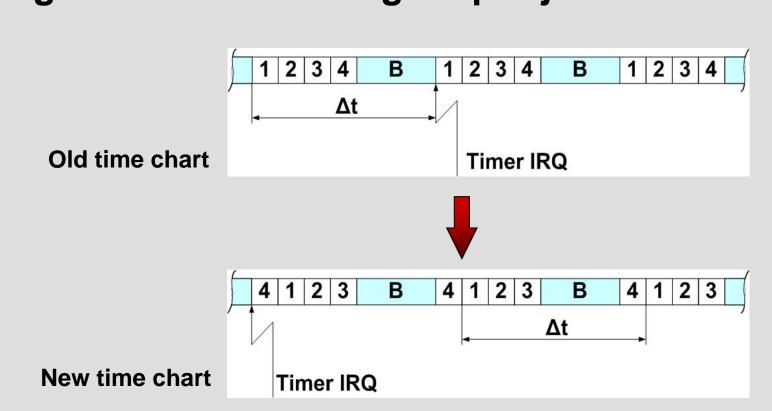
#### Analogous ICS -> time chart of operation



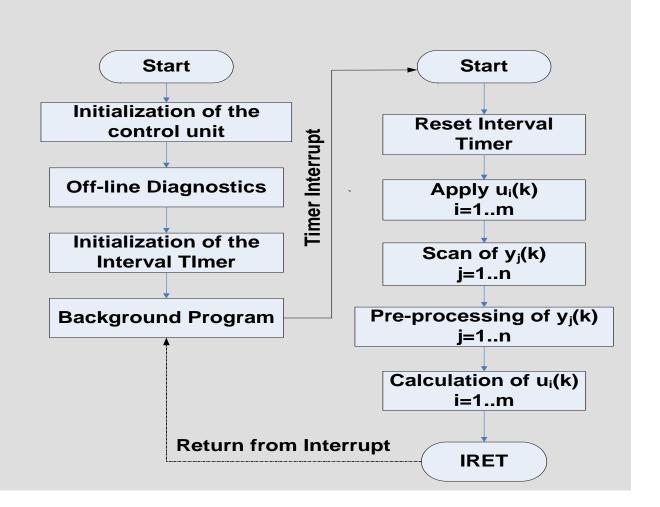
#### Analogous ICS -> how does it works

- Control device periodically executes a specific control task
- ♦ Period depends on the plant dynamics
- ♦ Period is based on interrupt requests
- Fluctuations of the moment for execution of phase 4 may occur

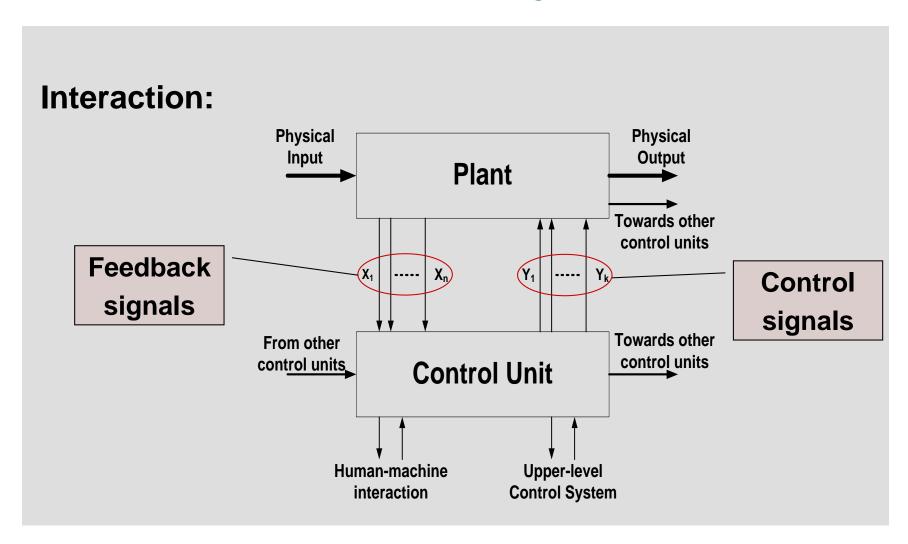
#### Analogous ICS -> avoiding output jitter



# Algorithm essentials







#### **Basics of the interaction:**

- ◆ Typical for the machinery construction industry and the transport industry.
- ♦ Both the plant and the control unit function discretely
- ♦ Exchanged signals are usually discrete
- Analogous signals used to create additional control conditions

#### Discrete Industrial Control Systems (DICS)

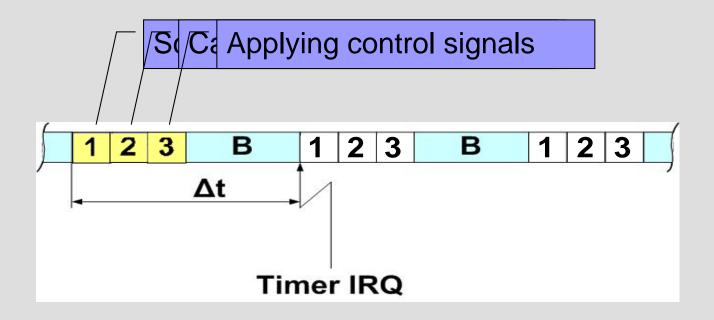
#### **Evolution:**

- ♦ Relay Schemes
- ♦ Digital Electronic Circuits
- ♦ Programmable Controllers
  - ► Programmable Logic Controllers
  - ► State Logic Controllers
- ♦ Synchronous vs. Asynchronous DICS

# Object and control device modeling and design

- Automata models
- Operational and control automaton
- Abstract synthesis of control automaton
  - Mealy Automata
  - Moore Automata
  - Petri Nets
  - etc.
- Structural synthesis of control automata

#### Programmable controller -> time chart



#### **DICS** programming

- 1. Different approaches for SLC and PLC
  - Abstract Synthesis for SLC
  - Structural Synthesis for PLC
- Languages -> specialized languages exist for both controllers types
  - ▶ PLC IEC 61131 Standards
  - ► SLC Petri Nets, FSM, or other visual language



